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Fri, 18 Aug 2006, 5:41:49 PM EST

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- #1 ((dynamically reconfigurable clock domain )<in>metadata)
- #2 (( clock domain)<in>metadata)
- #3 ((common clock domain)<in>metadata)
- #4 ((default and bypass)<in>metadata)
- #5 ((master clock )<in>metadata)
- #6 (((master clock)<in>metadata))<AND>(bypass or default<in>metadata))
- #7 ((handshake or (hand-shake) or handshaking or (hand-shaking))<in>metadata)
- #8 ((handshake or (hand-shake) or handshaking or (hand-shaking))<in>metadata)
- #9 (((((handshake or handshaking )<in>metadata))<and>(clock or domain or tree<in>metadata)))<AND>(bypass or default<in>metadata))
- #10 (((((handshake or handshaking )<in>metadata))<and>(clock or domain or tree<in>metadata)))<AND>(bypass or default<in>metadata))
- #11 ((bypass switch or bypass mux or bypass multiplexer )<in>metadata)
- #12 (((bypass switch or bypass mux or bypass multiplexer )<in>metadata))<AND>(clock<in>metadata))
- #13 clock distribution tree
- #14 (clock distribution tree) and domain
- #15 ((clock distribution tree) and bypass<IN>metadata)
- #16 ((clock distribution tree) and default<in>metadata)

- #17 (((handshake or (hand-shake) or handshaking or (hand-shaking))<in>metadata)) <AND> (clock distribution tree)
- #18 clock domain and common
- #19 ((clock domain and common)<AND>(clock domain and common<in>metadata))
- #20 (clock distribution tree) and skew

[REDACTED]

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<i>DB=PGPB, USPT; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L37	l3 and L34	52
<input type="checkbox"/>	L36	l24 and L34	5
<input type="checkbox"/>	L35	l24 L34	11042
<input type="checkbox"/>	L34	((write or writing) with bit) same bypass	527
<input type="checkbox"/>	L33	L32 and skew	129
<input type="checkbox"/>	L32	l24 and L31	308
<input type="checkbox"/>	L31	common near5 clock	12995
<input type="checkbox"/>	L30	L25 and master	103
<input type="checkbox"/>	L29	L28 and master	53
<input type="checkbox"/>	L28	l25 and skew	353
<input type="checkbox"/>	L27	l25 and (bypass and default)	24
<input type="checkbox"/>	L26	l24 and (handshake or handshaking or "hand-shake" or "hand-shaking")	137
<input type="checkbox"/>	L25	L24 and l3	640
<input type="checkbox"/>	L24	716/\$[ccls]	10520
<input type="checkbox"/>	L23	l13 not L20	56
<input type="checkbox"/>	L22	l12 not L20	138
<input type="checkbox"/>	L21	l6 not L20	324
<input type="checkbox"/>	L20	l1 or l2 or l7 or l8 or l10 or l11 or l14 or l17	144
<input type="checkbox"/>	L19	6738963[uref]	0
<input type="checkbox"/>	L18	L16 and skew	144
<input type="checkbox"/>	L17	L16 and l12	3
<input type="checkbox"/>	L16	l3 and L15	308
<input type="checkbox"/>	L15	bypass near5 (switch or multiplexer or mux)	10101
<input type="checkbox"/>	L14	L13 and skew	86
<input type="checkbox"/>	L13	l6 and (handshake or handshaking or "hand-shake" or "hand-shaking")	152
<input type="checkbox"/>	L12	(default near3 (path or mode)) same synchronization	142
<input type="checkbox"/>	L11	((dynamic or dynamically) near3 (reconfigure or reconfigurable or reconfiguring)) same (clock near5 (tree or domain))	3
<input type="checkbox"/>	L10	(master near3 (clock or signal)) same (bypass near3 (path or mode))	71
<input type="checkbox"/>	L9	master near3 (clock or signal)	29816

□	L8	L7 and skew	81
□	L7	l3 and L6	101
□	L6	l4 and L5	436
□	L5	default near3 (path or mode)	10548
□	L4	bypass near3 (path or mode)	12019
□	L3	clock near5 (tree or domain)	6067
□	L2	(6127874 6340905 6367060 5452239 6886121)[pn] or 20020040458	6
□	L1	6738963[pn]	1

END OF SEARCH HISTORY

## WEST Search History

DATE: Friday, August 18, 2006

Hide? Set Name Query Hit Count

*DB=PGPB; PLUR=YES; OP=OR*

<input type="checkbox"/>	L11	"common clock domain"[clm]	6
<input type="checkbox"/>	L10	("bypass path" with select\$)[clm]	46
<input type="checkbox"/>	L9	l5 and L8	2
<input type="checkbox"/>	L8	"bypass path"[clm]	192
<input type="checkbox"/>	L7	"default path"[clm]	20
<input type="checkbox"/>	L6	l2 and l5	3
<input type="checkbox"/>	L5	"master clock signal"[clm]	134
<input type="checkbox"/>	L4	l2 and domain[clm]	16
<input type="checkbox"/>	L3	l2 and tree[clm]	27
<input type="checkbox"/>	L2	"clock skew"[clm]	117
<input type="checkbox"/>	L1	"clock distribution tree"[clm]	11

END OF SEARCH HISTORY